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**Application Information**

Title Line One:: INTERCONNECT SUBSTRATE,  
Title Line Two:: SEMICONDUCTOR DEVICE, METHODS OF  
Title Line Three:: FABRICATING, INSPECTING, AND  
Title Line Four:: MOUNTING THE SEMICONDUCTOR DEVICE,  
Title Line Five:: CIRCUIT BOARD, AND ELECTRONIC  
Title Line Six:: INSTRUMENT  
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**Continuity Information**

>This application is a:: 371 of  
Application One:: PCT/JP00/06824  
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**Prior Foreign Applications**

Foreign Application One:: 11-281424  
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Country:: JAPAN  
Priority Claimed:: YES

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